IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appendient No.	6,911,371 B2
Patent Issue Date	June 28, 2005
Application Serial No.	
Filing Date	June 11, 2001
Assignee	Micron Technology, Inc.
Inventorship	Vishnu K. Agarwal et al.
Attorney's Docket No.	
Title: Capacitor Forming Methods with Barrier Lay Inducing Material	yers to Threshold Voltage Shift

REQUEST FOR CERTIFICATE OF CORRECTION OF PATENT FOR PTO MISTAKE (37 C.F.R. 1.322(a))

То:	Commissioner for Patents P.O. Box 1450	FEB 2 3 2006
ATTN:	Alexandria, VA 22313-1450 Decision and Certificate of Correction Branch of the Patent Issue Division	of Correction
From:	James E. Lake (Tel. 509-624-4276; Fax 509-8	38-3424)

From: James E. Lake (Tel. 509-624-4276; Fax 509-838-3424) Wells St. John P.S. 601 W. First Avenue, Suite 1300 Spokane, WA 99201-3828

Sir:

It is hereby requested that a Certificate of Correction be issued with respect to Patent No. 6,911,371 B2, granted June 28, 2005, in accordance with the Certificate of Correction form attached hereto in duplicate.

The errors listed on the Certificate of Correction form were apparently incurred through the fault of the PTO as will be disclosed by the records of files in the Office.

Since this Certificate of Correction is being requested due to PTO errors, it is believed that no fee is due. However, in the event that a fee is required for issuance of this Certificate of Correction, please charge the fee specified under

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37 C.F.R. § 1.20(a) to Deposit Account No. 23-0925. Please credit Deposit Account No. 23-0925 with any overpayment of the above fee.

Attached hereto, in duplicate is Form PTO-1050, with at least one copy being suitable for printing.

Respectfully submitted,

Dated: 16 Feb 2006

By:

James Reg. No. 44,854

Page <u>1</u> of <u>2</u>

PATENT NO. : 6,911,371 B2

APPLICATION NO.: 09/879,335

DATED : June 28, 2005

INVENTOR(S) : Vishnu K. Agarwal et al.

It is certified that an error or errors appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Face page (75) Inventors

Add: Kunal R. Parekh, Boise, ID (US); and Randhir P.S. Thakur, San Jose, CA (India)

Face page (56) References cited add the following patents to the list of U.S. Patent Documents:

5,796,166	8/1998	Agnello	257/751
5,699,291	12/1997	Tsunemine	438/240
5,608,249	3/1997	Gonzalez	257/306
6,165,833	12/2000	Parekh et al	438/240

Related Patent Data, column 1, line 12 Replace "a continuation application of U.S. Ser. No. 08/944,054" With --a continuation application of U.S. Ser. No. 08/994,054--

Column 1, line 35 Replace "A shift threshold voltage (V_t) " With --A shift in threshold voltage (V_t) --

Mailing Address of Sender: James E. Lake Wells St. John P.S. 601 West First Avenue, Suite 1300 Spokane, WA 99201-3828 Patent No. <u>US 6,911,371 B2</u>

FFR 27 AUD PAT-US\CR-01

Page <u>2</u> of <u>2</u>

PATENT NO. : 6,911,371 B2

APPLICATION NO.: 09/879,335

DATED : June 28, 2005

INVENTOR(S) : Vishnu K. Agarwal et al.

It is certified that an error or errors appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 4, line 13 Replace "being NO----." With --being NO-.--

Column 4, line 19 Replace "particularly NO—. Diffusion of impurities and other" With --particularly NO-. Diffusion of impurities and other--

Column 8, line 4 Replace "One potential advantage of wafer construction and wafer" With --One potential advantage of wafer 50 construction and wafer--

Column 7, line 63 Replace "FIG. 13 shows wafer construction after" With --FIG. 13 shows wafer construction 50 after--

Column 7, line 59 Replace "FIG. 12 shows a wafer construction including wafer" With --FIG. 12 shows a wafer construction 50 including wafer--

Mailing Address of Sender: James E. Lake Wells St. John P.S. 601 West First Avenue, Suite 1300 Spokane, WA 99201-3828

Patent No. <u>US 6,911,371 B2</u>

Page <u>1</u> of <u>2</u>

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Column 1, line 35 Replace "A shift threshold voltage (V_t) " With --A shift in threshold voltage (V_t) --

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Column 7, line 59 Replace "FIG. 12 shows a wafer construction including wafer" With --FIG. 12 shows a wafer construction 50 including wafer--

Mailing Address of Sender: James E. Lake Wells St. John P.S. 601 West First Avenue, Suite 1300 Spokane, WA 99201-3828 Patent No. <u>US 6,911,371 B2</u>

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Mi22-1568 (jcl) Micron Technology, Inc.

August 13, 2002

The stamp of the U.S. Patent and Trademark Office acknowledges receipt of the following papers relating to the Patent application of Vishnu K. Agarwal, et al, Serial No. 09/879,335, filed June 11, 2001, entitled "Capacitor Forming Constructions". ų.

1.	Return Postcard Receipt;
2.	Transmittal Form PTO/SB/21;
З.	Amendment, Petition, and Fee to Add Inventors;
4.	Statement of Non-Deceptive Intent (2) originals;
5.	Inventors Declaration (2 originals);
6.	Assent of Assignee to Addition of Inventors;
7,	Copy of Notice of Recordation of Assignment Document;
8,	Assignment Recordation Cover Sheet;
9.	Assignment of Invention (Thakur/Parekh, 2 originals);
10.	Fee Transmittal Form PTO/8B/17;
11.	A \$170.00 check (Petition & Assignment fee);
12.	Power of Attorney by Assignce w/statement under 37 CFR 3.73
13.	Copy of Gealy/Agarwal Assignment;
14.	Copy of Board of Directors Resolution.





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actitioner's Docket No. MI22-1568

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:	Agarwal, Vishnu K.
Application No.:	09/879,335
Group No.:	2813
Filed:	June 11, 2001
Examiner:	Y. Huynh
For:	Capacitor Forming Methods and Capacitor Constructions

Assistant Commissioner for Patents Art Unit 2813 Washington, D.C. 20231

AMENDMENT, PETITION AND FEE TO DELETE AND/OR ADD TO ORIGINAL ERRONEOUSLY NAMED OR NOT NAMED INVENTOR(S) IN DECLARATION--NONPROVISIONAL APPLICATION--(37 C.F.R. Section 1.48(a))

- 1. This amendment and petition is to correct the incorrect original naming of inventor(s) in the declaration under 37 C.F.R. section 1.48(a) as set forth and filed on June 11, 2001.
- 2. Addition and/or Deletion of Inventor(s)

Add the following previously unnamed person(s) as inventor(s) of this application:

Kunal R. Parekh Randhir P.S. Thakur

CERTIFICATE OF MAILING/TRANSMISSION (37 C.F.R. section 1.8(a))

I hereby certify that, on the date shown below, this correspondence is being:

MAILING

deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231.

FACSIMILE

□ transmitted by facsimile to the Patent and Trademark Office.

F 1- 1 2

Date:

Signature

(type or print name of person certifying)

(Amendment, Petition and Fee to Delete and/or Add to Original Erroneously Named or Not-Named Inventor(s) in Declaration--Nonprovisional Application -- (37 C.F.R. section 1.48(a)) -- page 1 of 2) 3. Attachments:

(a) A statement from each person being added as an inventor that the error in inventorship occurred without deceptive intention on his or her part. 37 C.F.R. section 1.48(a)(1).

(b) A declaration by each of the actual inventor(s) as required by 37 C.F.R. section 1.63 (or as permitted by sections 1.42, 1.43, OR 1.47). 37 C.F.R. section 1.48(a)(2).

(c) Written consent of the assignee. 37 C.F.R. section 1.48(a)(4).

4. Fee Payment (37 C.F.R. section 1.17(I))

The fee required by 37 C.F.R. section 1.17(I) is paid as follows:

Enclosed is a check for \$130.00. Charge Account No. 23-0925 for any fee deficiency.

13 Aug Date: NUL

James E. Lake Registration No. 44,854 Wells St. John P.S. 601 West First Avenue, Suite 1300 Spokane, WA 99201 509-624-4276

FFR 2

Customer No. 021567



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.:	09/879,335
Date of filing:	June 11, 2001
Name of Patentee:	Micron Technology, Inc.
Title of Invention:	Capacitor Forming Methods and Capacitor Constructions

Assistant Commissioner for Patents Washington, D.C. 20231 ATTENTION: Supervisory Patent Examiner, Unit 2813

STATEMENT OF NON-DECEPTIVE INTENT BY PERSON BEING ADDED BY PETITION TO CHANGE INVENTORSHIP IN PATENT APPLICATION (37 C.F.R. Section 1.48(a)(2)

I, the person who is being added as an inventor by the petition being submitted to correct the inventorship of this patent application do hereby declare that the inventorship error in failing to include my name as an inventor on this patent application occurred without any deceptive intention on my part.

Date: _____

Kunal R. Parekh

Date: 7/37/07

the BS Raten

Randhir P.S. Thakur

(Statement of Non-deceptive Intent by Person Being Added by Petition to Change Inventorship in Patent--page 1 of 1)



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: 09/879,335

Date of filing: June 11, 2001

Name of Patentee: Micron Technology, Inc.

Title of Invention: Capacitor Forming Methods and Capacitor Constructions

Assistant Commissioner for Patents Washington, D.C. 20231 ATTENTION: Supervisory Patent Examiner, Unit 2813

STATEMENT OF NON-DECEPTIVE INTENT BY PERSON BEING ADDED BY PETITION TO CHANGE INVENTORSHIP IN PATENT APPLICATION (37 C.F.R. Section 1.48(a)(2)

I, the person who is being added as an inventor by the petition being submitted to correct the inventorship of this patent application do hereby declare that the inventorship error in failing to include my name as an inventor on this patent application occurred without any deceptive intention on my part.

Date: 7/10/02

ful

Kunal R. Parekh

Date:

Randhir P.S. Thakur

(Statement of Non-deceptive Intent by Person Being Added by Petition to Change Inventorship in Patent -- page 1 of 1)

FEB 2 1	1006					
FEB 2 1	Ander the Paperwork Reduction Act of 1995	5. no person:	U.S. I s are required to respond to a co	Patent and T	rademark Office; U	PTO/SB/21 (09-04) through 07/31/2006. OMB 0651-0031 S. DEPARTMENT OF COMMERCE displays a valid OMB control number.
TRADE			Application Number	09/879,33	5 (US Patent No. 6	,911,371 B2)
	TRANSMITTAL		Filing Date	June 11, 2	2001 (issued on Jur	ne 28, 2005)
	FORM		First Named Inventor	Vishnu K.	Agarwal et al.	
			Art Unit	2813		
	(to be used for all correspondence after initial	filing)	Examiner Name	C. Thomps	son	
Į	Total Number of Pages in This Submission		Attorney Docket Number	MI22-1568	3	
		ENCI	LOSURES (Check all	that apply	1	
	Fee Transmittal Form Fee Attached Amendment/Reply After Final Affidavits/declaration(s) Extension of Time Request Express Abandonment Request Information Disclosure Statement Certified Copy of Priority Document(s) Reply to Missing Parts/ Incomplete Application Reply to Missing Parts under 37 CFR 1.52 or 1.53		Drawing(s) Licensing-related Papers Petition Petition to Convert to a Provisional Application Power of Attorney, Revocatio Change of Correspondence A Ferminal Disclaimer Request for Refund CD, Number of CD(s) Landscape Table on CD ks r No. 021567 no fee believed due, howeve er 37 C.F.R. 1.16 and 1.17 w	on Address 	After A Appeal of Appeal Appeal Appeal Appeal Appeal Appeal Proprie Status V Other E below): PTO Return M Request for O Certificate of Copies of pre	Enclosure(s) (please Identify Receipt Postcard Cert. of Correction - PTO errors Correction in duplicate wiously filed documents
	SIGNA		F APPLICANT, ATTO	RNEY O		
	Firm Name Wells St. John P.S.					
	Signature	Ac				
	Printed name James E. Lake					
	Date 16 Feb 2	2006	F	Reg. No.	44,854	
ĺ	CERTIFICATE OF TRANSMISSION/MAILING I hereby certify that this correspondence is being facsimile transmitted to the USPTO or deposited with the United States Postal Service with					
	sufficient postage as first class mail in an en the date shown below: Signature	velope add				
(Typed or printed name Racquel M. Espin)			Date	16 Feb. 2006

This collection of information is required by 37 CFR 1.5. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.11 and1.14. This collection is estimated to 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

If you need assistance in completing the form, call 1-800-PTO-9199 and select option 2.